

Patented Mar 16 1909

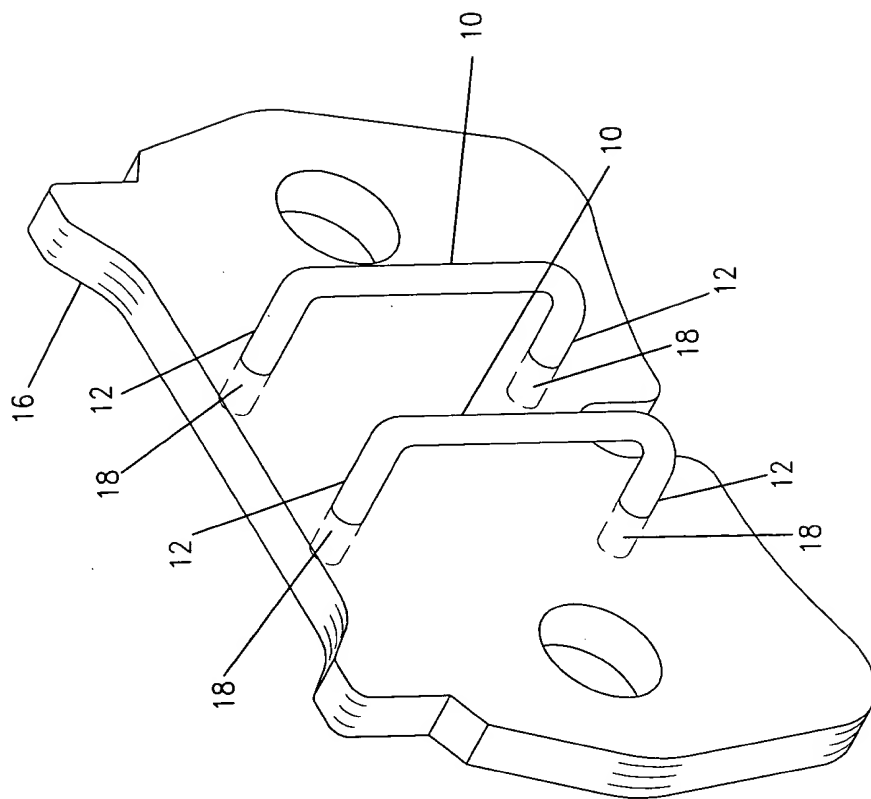


FIG. 1

FIG. 2

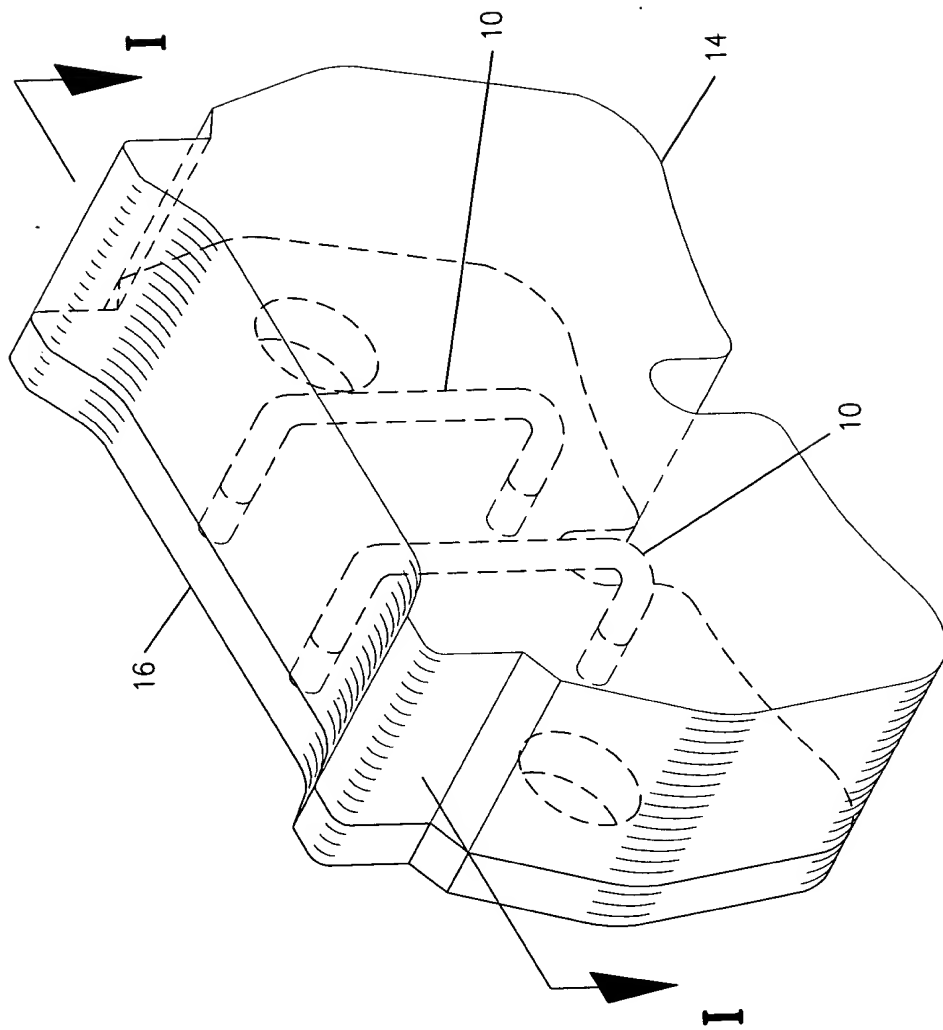


FIG. 2

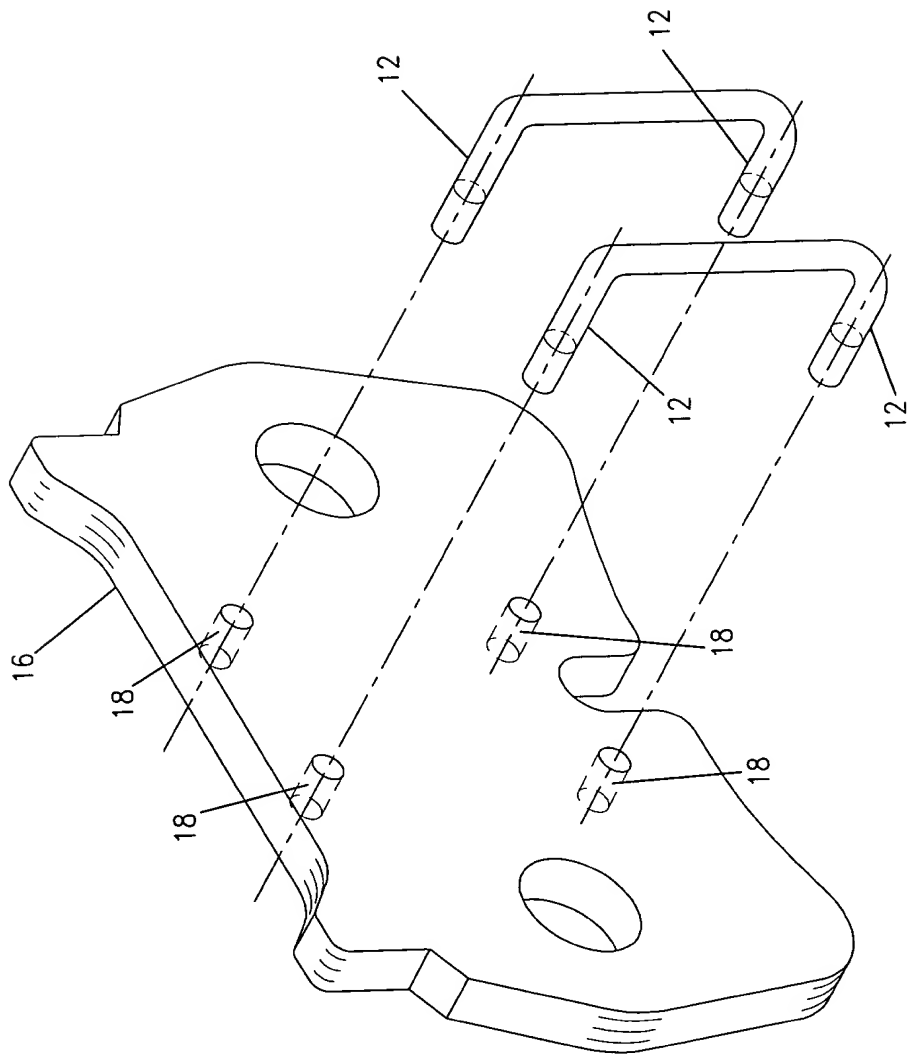


FIG. 3

FIG. 4 is a cross-sectional view of the device 100 in a second state, where the device 100 is in a compressed state. The device 100 includes a substrate 14, a layer 10, and a layer 16. The layer 10 is a conductive layer, and the layer 16 is a dielectric layer. The device 100 is shown in a cross-sectional view, and the layers 10 and 16 are shown in a cross-sectional view.

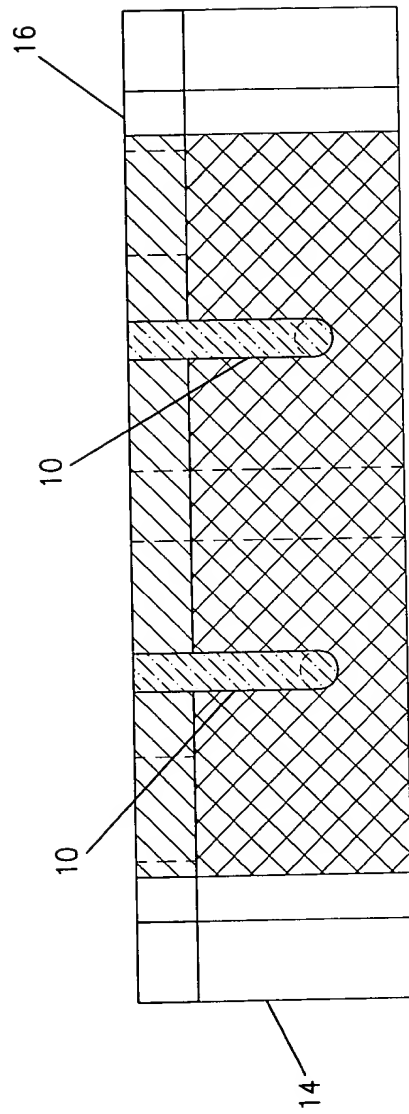


FIG. 4